

<b>Notice of References Cited</b>	Application/Control No. 10/812,003	Applicant(s)/Patent Under Reexamination KU ET AL.	
	Examiner Cheung Lee	Art Unit 2812	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,518,631	02-2003	En et al.	257/382
	B	US-6,458,702	10-2002	Aloni, Efraim	438/682
	C	US-6,329,276	12-2001	Ku et al.	438/586
	D	US-2004/0123922	07-2004	Cabral et al.	148/527
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, Stanley; "Silicon Processing for the VLSI ERA" Volume 2: Process Integration; 1990 by Lattice Press.; Pages 104-105
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.